FuturePlus Systems Introduces 1066 MT/s DDR3 Memory Bus Analysis Probe

Only DDR3 interposer probe with proven reliable 1066 MT/s data capture.

COLORADO SPRINGS, Colo., November 10, 2006, -- FuturePlus Systems Corp. today introduced the FS2350 DDR3 1066 interposer analysis probe for next-generation double-data-rate (DDR) SDRAM buses. It will be used by designers involved with DIMM validation, failure analysis and bus functional-parametric validation in PC gaming, servers, super computing, and high-definition television.

The new probe is designed for use with Agilent logic analysis systems to provide state analysis and protocol decode at up to 1066 megatransfers per second (MT/s), as well as timing analysis at up to 4 GHz in systems with typical data valid windows as narrow as 650 ps.

FuturePlus Systems Corp. believes that the FS2350 is the only DDR3 interposer analysis probe proven to provide reliable 1066 MT/s data capture.

Interposer Design Provides Flexibility and Ease of Use.

The new probe offers flexibility and out-of-box ease-of-use. Its low-profile interposer design minimizes probe-induced effects on the DDR3 bus. An interposer probe is inserted into the target memory socket, while also accepting a memory module in the probe's extender DIMM slot. This capability is essential when the target system has only one memory slot, or when a user must probe a fully-populated memory bus.

Software Components Speed Setup and Simplify Analysis.

The FS2350 includes software that helps users to comprehend acquired data and enhances usability. Protocol-decode software interprets bus data and displays it in protocol, hexadecimal, or binary format; and auto-configuration files facilitate quick logic-analyzer setup.

Supported DIMMs

The FS2350 supports a 240-pin, 533 MHz clock (1066 MT/s data rate), 64-bit, Unbuffered Synchronous Double Date Rate 3 DRAM Dual In-Line Memory Module (DDR3 SDRAM DIMM).

U.S. Pricing and Availability -- The FS2350 has a U.S. list price of \$40,000. Delivery is four weeks ARO.

About DDR3 Memory

DDR3 is the newest generation of double-data-rate memory technology. Designed to meet the increasing demand for greater performance in such applications as PC gaming, servers, super-computing, notebooks and high-definition television, its main advantages are its higher data rate and lower power consumption than earlier DDR memory.

DDR3 devices will initially operate at 800 - 1066 MT/s, (with clock rates of 400 MHz to 533 MHz, respectively). Future devices are expected to reach speeds up to 1600 Mb/s by year 2009. At DDR3's top speed, 70 copies of Tolstoy's War and Peace would be transferred in about one second.

About FuturePlus Systems

FuturePlus Systems Corporation is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is an Agilent Technologies channel partner. FuturePlus Systems products are also supported by authorized resellers in Canada, China, Taiwan, Japan, India, Italy, United Kingdom and most European countries. More information about FuturePlus Systems Corporation may be found on the internet at www.futureplus.com.

###